

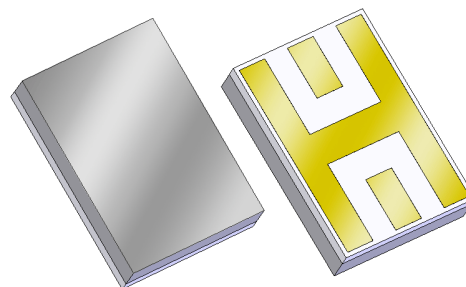


880157

2560 MHz BAW Filter

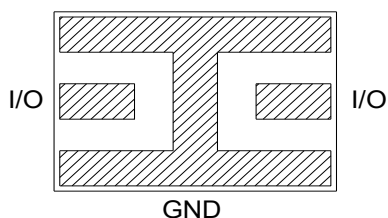
General Description

880157 is 2560 MHz RF BAW filter designed in a small hermetic package for S-Band and high selectivity applications.



CSP: 3.71 x 2.57 x 0.84 mm

Functional Block Diagram



Bottom View

Pin Configuration - Single Ended

Pin No.	Label
I/O	Input / Output
GND	Ground

Product Features

- Usable bandwidth of 30 MHz
- Low Loss
- High Selectivity
- Single-Ended Operation
- 50 Ω Impedance at Input / Output
- Ceramic Chip-Scale Package (CSP)
- Small Size
- Hermetically Sealed

Applications

- S-Band
- For high selectivity applications

Ordering Information

Part No.	Description
1058426	2560 MHz BAW Filter – Waffle Pack
1099896	2560 MHz BAW Filter – 7" Reel
1072902	Evaluation board



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Absolute Maximum Ratings

Parameter	Rating
Storage Temperature	-55 to 100 °C
Operation Temperature	-40 to +85 °C
RF Input Power ⁽¹⁾ - Test conditions: PW = 200ms; DC = 50% @ +25 °C	41 dBm

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to device may reduce device reliability.

⁽¹⁾ Input Power for both Input & Output ports

Minimum Lifetime Ratings

Conditions	Rating
RF Input Power ⁽¹⁾ , for both Input and Output ports	>500K hours

⁽¹⁾ Input Power: CW, 20 dBm, @ +55 °C

Electrical Specifications ⁽¹⁾

Test conditions unless otherwise noted: ⁽²⁾ Temp = -40 to +85 °C

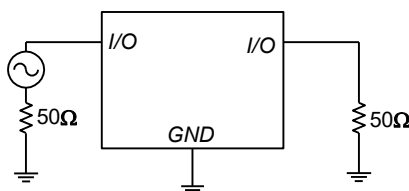
Parameter ⁽³⁾	Conditions	Min	Typical ⁽⁴⁾	Max	Units
Center Frequency		-	2560	-	MHz
Maximum Insertion Loss	@ Fo	-	3	4.5	dB
3 dB Bandwidth	Reference Loss @ Fo	30	48	-	MHz
40 dB Lower Frequency Edge		2505	2521	-	MHz
40 dB Upper Frequency Edge		-	2598	2610	MHz
Input/Output VSWR	@ Fo	-	1.4:1	2:1	-
Source Impedance ⁽⁵⁾	Single-ended	-	50	-	Ω
Load Impedance ⁽⁵⁾	Single-ended	-	50	-	Ω

Notes:

1. All specifications are based on the Qorvo schematics for the reference designs shown on page 3.
2. Devices tested at room temperature to a guard band specification to ensure electrical compliance over temperature range.
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances.
4. Typical values are based on average measurements at room temperature (25 °C ±5 °C).
5. Optimum impedance to achieve the performance shown

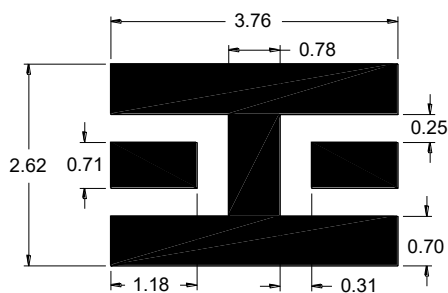
Matching Schematics

50 Ω
Single-ended
Input



50 Ω
Single-ended
Output

PCB Mounting Pattern

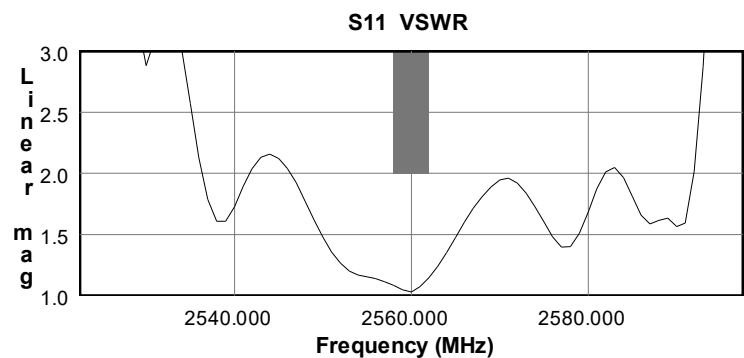
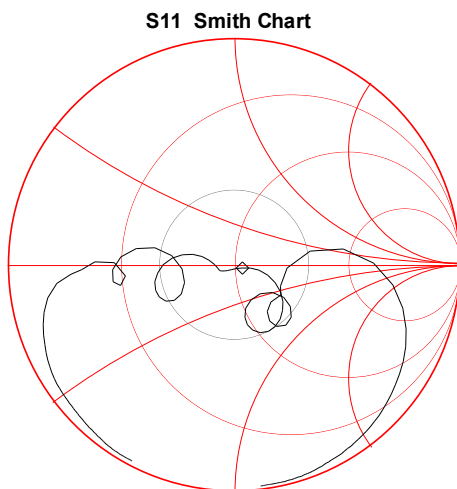
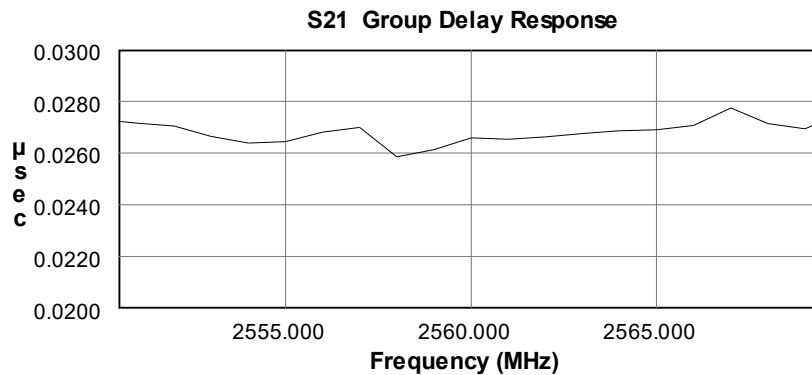
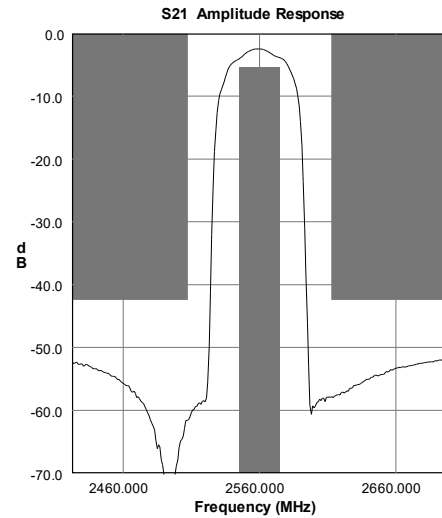
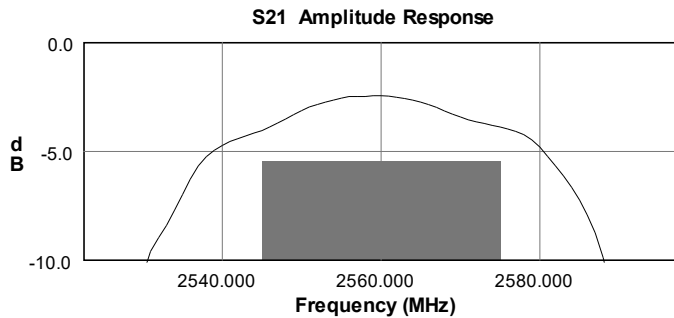


Notes:

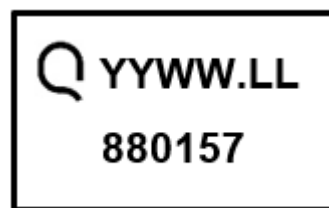
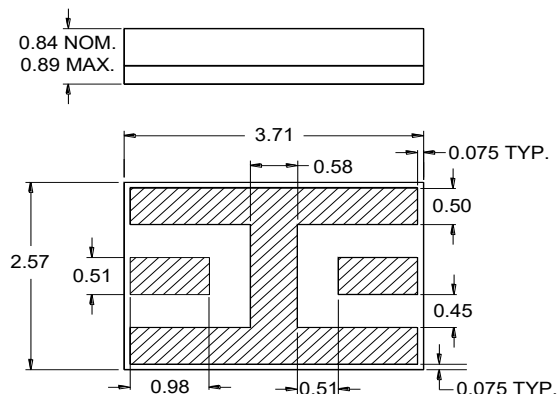
1. All dimensions are in millimeters.
2. Modifications may be necessary to suit end user assembly materials and processes.

Typical Performance

Test conditions unless otherwise stated: Temp. = 25 °C ±5 °C



Device Package Information, Marking and Dimensions



Package Style: CSP
Dimensions: 3.71 x 2.57 x 0.84 mm

Package Base: Sapphire
Package Lid: Alumina
Terminations: Au plating over Ni (0.33 ~ 0.83 μm Au, 2.0 ~ 6.0 μm Ni)

All dimensions shown are nominal in millimeters.
All tolerances are $\pm 0.13\text{mm}$ except overall length and width $\pm 0.25\text{mm}$.
Overall width, length, and thickness are the only critical dimensions. All other dimensions are for reference only.

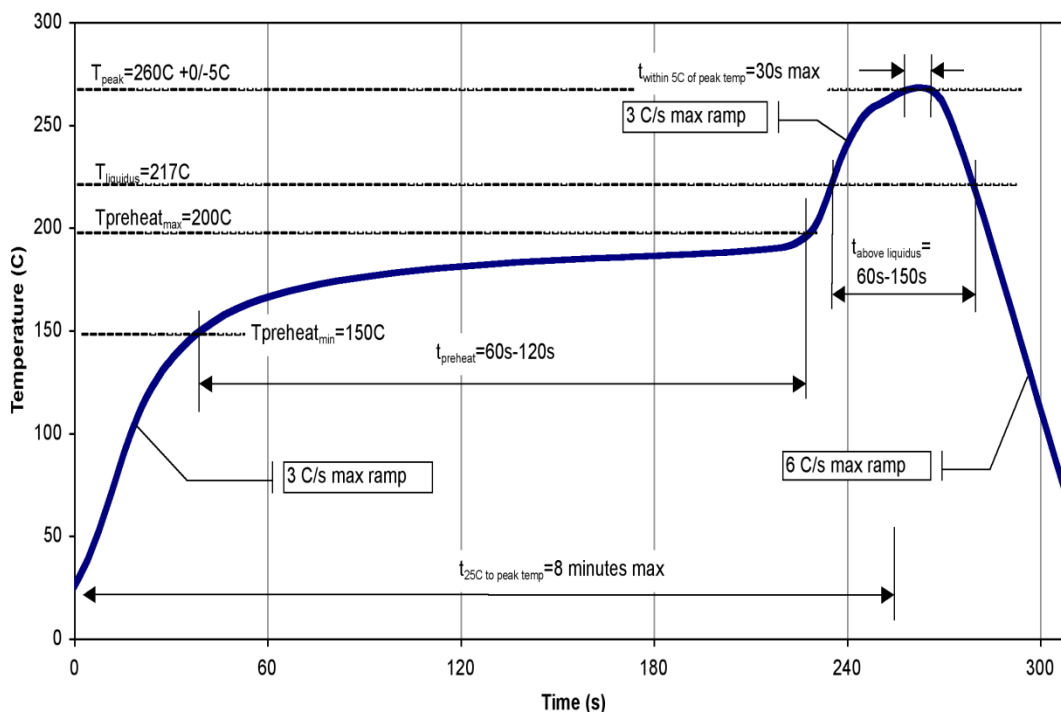
Marking includes corporate logo, date code, and product part number.
The date code consists of, YY = last 2 digits of the year, WW = 2 digits of calendar work week and LL = Lot ID, unique lot identifier.
Marking Diagram is for Reference Only.

Tape and Reel Information

- Tape and Reel per EIA-481 available. Additional information is available upon request.
- Solder tinning is available per IPC J-STD-001.

1. Compatible with both Lead-free solder (260°C peak reflow temperature) and tin/lead (245°C peak reflow temp.) soldering processes.
2. Contact plating: Au plating over Ni.

Recommended Soldering Profile





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Handling Precautions

Parameter	Rating	Standard
ESD – Human Body Model (HBM)	Class 3B	ANSI/ESD/JEDEC JS-001
ESD – Charged Device Model (CDM)	Class C3	ANSI/ESD/JEDEC JS-002
MSL – Moisture Sensitivity Level	N/A, Hermetic Package	



Caution!
ESD-Sensitive Device

RoHS Compliance

This product is compliant with the 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment), as amended by Directive 2015/863/EU. This product also has the following attributes:

- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄O₂) Free
- PFOS Free
- SVHC Free

Contact Information

For the latest specifications, additional product information, worldwide sales and distribution locations:

Web: www.qorvo.com

Tel: 1-844-890-8163

Email: customer.support@qorvo.com

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